

MAC ARTHUR C. RAMILO



Contact

Address:

129 St. Joseph Street, Miguel Mojica,
Mendez, Cavite, Philippines

Email:

macarthurramilo@gmail.com

LinkedIn:

[https://www.linkedin.com/in/mac-
arthur-ramilo-b82830196/](https://www.linkedin.com/in/mac-arthur-ramilo-b82830196/)

Professional Skills

- Circuit Design and Analysis
- Electronic System Troubleshooting
- Telecommunications
- Signal Processing
- Digital Communication Protocols
- Project Management
- Technical Specification Development
- PCB Design
- Microcontroller Programming
- Quality Assurance and Compliance
- Problem Solving
- Attention to Detail
- Effective Communication
- Cross-functional Collaboration
- Time Management
- Technical Documentation
- Testing and Validation

Summary

I am a highly skilled Electronics & Communication Engineer with a strong background in designing, testing, and troubleshooting electronic systems. My expertise lies in analyzing complex circuits, ensuring optimal performance, and implementing innovative solutions. Throughout my career, I have developed a deep understanding of telecommunications, signal processing, and digital communication protocols. I am dedicated to delivering excellence and have consistently completed projects within tight deadlines while maintaining the highest quality standards. Collaborating with cross-functional teams, I excel at identifying requirements, developing technical specifications, and executing projects from concept to completion. I am proficient in utilizing industry-standard software tools for circuit simulation, PCB design, and programming microcontrollers. With my exceptional problem-solving abilities and meticulous attention to detail, I have a proven track record of resolving technical issues and improving system efficiency. Strong communication and interpersonal skills allow me to effectively convey complex ideas to both technical and non-technical stakeholders. I am now seeking an opportunity to leverage my expertise and contribute to the development of advanced electronic systems in a challenging environment.

Work Experience

- **OSP Senior Design Engineer | FIBERHOME PHILS. INCORPORATED | Makati City, Metro Manila | March 11, 2020 – October 3, 2022**

Duties:

- Conducting Site Survey at field, communicating with clients on project dealing with equipment installation and recording information on site
- Providing propose and as-built plans that serves as a basic drawing to the project, such as OSP FTTx, FTTU, FTTB, FTTH, FTTBTS and IBS. Perform route analysis and develop aerial and underground fiber route maps.
- Perform route analysis and develop aerial and underground fiber route maps. Perform route analysis and develop aerial and underground fiber route maps, and underground plans for construction works like open trench (manual or mechanized) and horizontal direct drilling or trenchless methodology.
- Understand the telecommunication industry standards, local telecom practices and procedures, local telecom practices and procedures, local city and governmental rules and regulations.

Personal Skills

- Hardworking and can work independently
- Obedient, respectful and submissive to superiors
- Cooperative with peers
- Responsive to a given task
- Problem-solving abilities
- Computer Proficiency

Government Examinations

➤ **Boards of Electronics Engineers**

Date of Examination: Oct 19 - 20, 2019

Board Rating: **PASSED**

➤ **Boards of Electronics Technicians**

Date of Examination: Oct 21, 2019 Board Rating: **PASSED**

Driving License

Non-Professional Driving License: **A, A1, B1.**

Personal Information

Date of Birth: Feb 12, 1999

Birthplace: Imus, Cavite

Citizenship: Filipino

Marital Status: Married

Languages:

- English: Proficient
- PTE Examination:**
Overall: 77
Listening: 75
Reading: 79
Writing: 73
Speaking: 90

Date of Examination: June 5 2023

- Filipino: native language

➤ **OSP Engineer| SUNIWAY GROUP OF COMPANIES, INC. | CCP Complex, Pasay City, Metro Manila | Oct 5, 2022 – July 14, 2023**

Duties

- Providing design plans using computer-aided design (CAD) software to create prototypes. Determines the scope, budget, and schedules for existing and new OSP projects, assumes full responsibility for the safety and security of the existing OSP facility, submits weekly project status reports to the reporting managers.
- Implementation and installation from Optical Line Terminal (OLT) to Optical Network Unit (ONU).
- Develops aerial and underground fiber route maps and executes tasks such as route analysis and assumes charge of fiber optic verification and all cable route mapping activities.
- Familiar with fiber optic cables, OLT, ONU, LCP, NAP, closure, ODF, hardware, pole, duct, manhole, handhole, etc.
- Basic knowledge in operating splicing equipment (Fusion and Mechanical), Optical Time Domain Reflectometer (OTDR) and Power Meter.
-

➤ **Associate Engineer-Product Engineering| Analog Devices Inc.| General Trias City, Cavite, Philippines| July 24, 2023 – Present**

Duties:

- Responsible in maintaining the overall product health of LTC Products (Automotive and Non-automotive).
- Primarily deals with Manufacturing line issues (e.g. yield issue, quality, sampling failures, program related, material and equipment).
- Product management and disposition on issues encountered on manufacturing.
- Yield sustaining and improvements of owned products.
- Datasheet and Test limit settings.
- Bench testing using oscilloscope, power supply and multi meter.
- Program releasing on addressing issues on manufacturing, improvement initiatives, and new product release.

Education

- **Bachelor Of Science in Engineering and Communication Engineering | Don Severina de las Alas Campus, Indang, Cavite | Cavite State University | June 2014 - June 2019**

Profile Summary

- Accomplished Electronics & Communication Engineer with a strong aptitude for designing, testing, and troubleshooting electronic systems.
- Extensive experience in analyzing intricate circuits and ensuring optimal functionality.
- Proficient in telecommunications, signal processing, and proficient with various digital communication protocols.
- Track record of successfully collaborating with diverse teams to identify project requirements and develop detailed technical specifications.
- Demonstrated ability to deliver projects on time while upholding exceptional quality standards.
- Skilled in utilizing industry-standard software tools for circuit simulation, PCB design, and programming microcontrollers.
- Exceptional communication and interpersonal skills, facilitating effective interaction with both technical and non-technical stakeholders.

Character Reference

Alex P. Credo

Regional Project Manager-SLZ FTTB
Fiberhome Philippines Incorporated Makati
City, Metro Manila (+63) 917-657-1458

Daryll Ayen

Senior Supervisor, Product Engineering
ADPhils-F2 TPE, Analog Devices, Inc.,
daryll.ayen@analog.com

Carl De Guzman

Staff Engineer, Product Engineering
ADPhils-F2 TPE, Analog Devices, Inc.,
carl.deguzman@analog.com

Projects

➤ Design and Planning of FTTH Project | OSP Engineer | Suniway Group of Companies|San Pedro, Laguna, Philippines| January 2023 – June 2023

Objectives:

- Planning & Modeling FTTH for boosting internet connectivity & speed up the highest possible transmission of data utilizing the technology of FTTH
- Minimizing the risks associated with the anticipated scheme by coordinating with different HOA & local governments.

➤ Modeling of Slotted Microstrip Patch Antenna | Don Severino De Las Alas Campus | Cavite State University | Indang, Republic of the Philippines | August 2018 to October 2018

Objectives:

- Modeling a slotted MPA for improving the gain of the antenna with appropriate dimensioning of substrates & optimization of the patch dimensions.
- Reducing the loss of transmission with the effective matching of impedances between feedline & patches.

➤ Design of Digital Modulation/Demodulation Schemes | Don Severino De Las Alas Campus | Cavite State University | Indang, Republic of the Philippines | February 2018 to April 2018

Objectives:

- Upsurging & enhancing the spectral efficiency of the system with an application of digital modulation scheme.
- Mitigating impacts of interferences & noises with the integration of error-correcting codes & techniques.

Declaration

I, hereby, declare authenticity of the information provided to be true and genuine to the best of my ability and knowledge.



Mac Arthur C. Ramilo